



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

TI-34177

Andrew Marshall

Art Unit: 2827

Serial No: 10/626,438

Examiner: Thong Quoc Le

Filed: 07/24/03

For: Circuit for Reducing Standby Leakage in a Memory Unit

AMENDMENT PURSUANT TO 37 CFR 1.312

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Karen Vertz
Karen Vertz

6-2-05
Date

Dear Sir:

In response to the Notice of Allowability of May 27, 2005, please amend the above-identified application as follows: